

FIG. 2

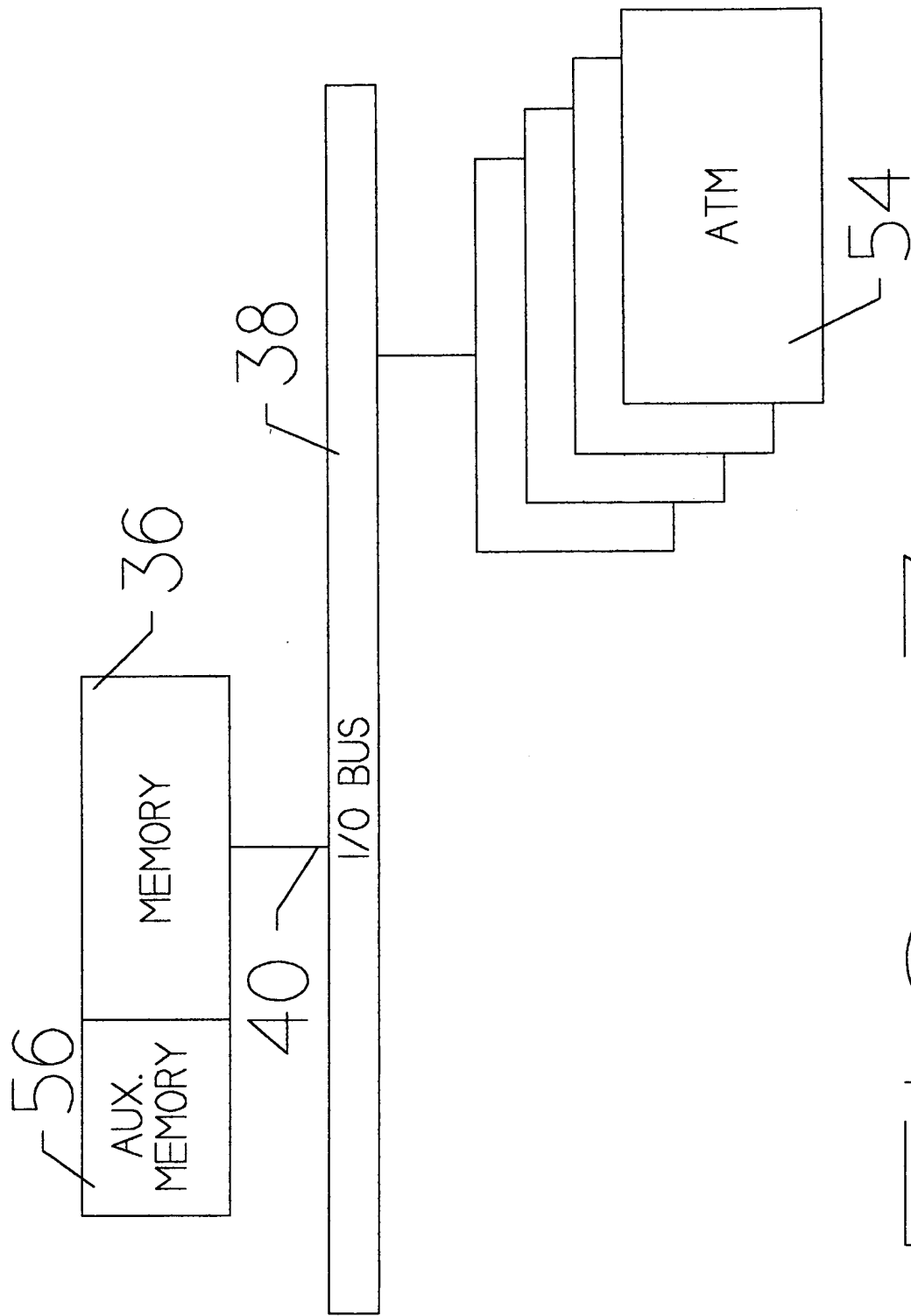


FIG. 3

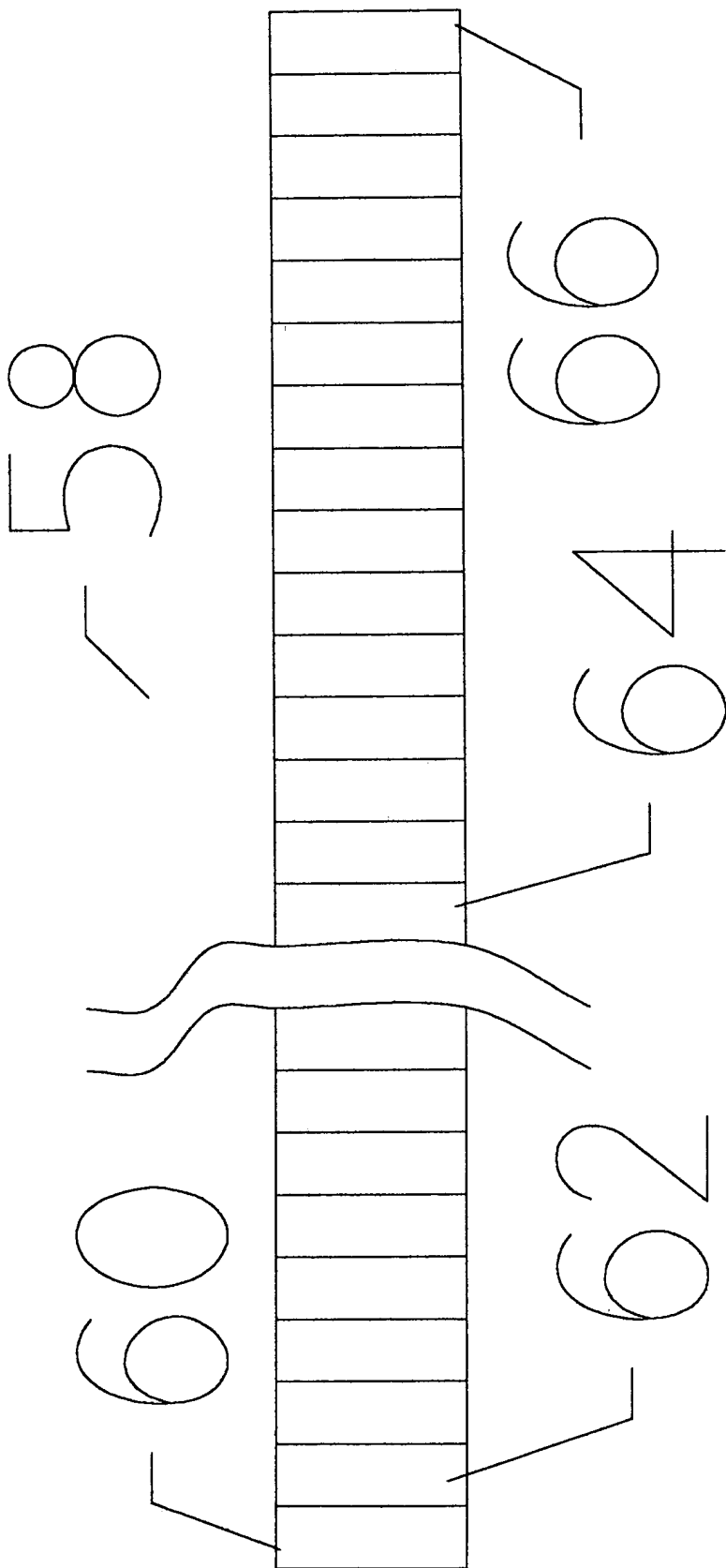


FIG. 5 is a block diagram of a system 500. The system 500 includes a processor 502, a memory 504, a network interface 506, and a user interface 508. The processor 502 is connected to the memory 504, the network interface 506, and the user interface 508. The network interface 506 is connected to a network 510. The user interface 508 is connected to the processor 502.

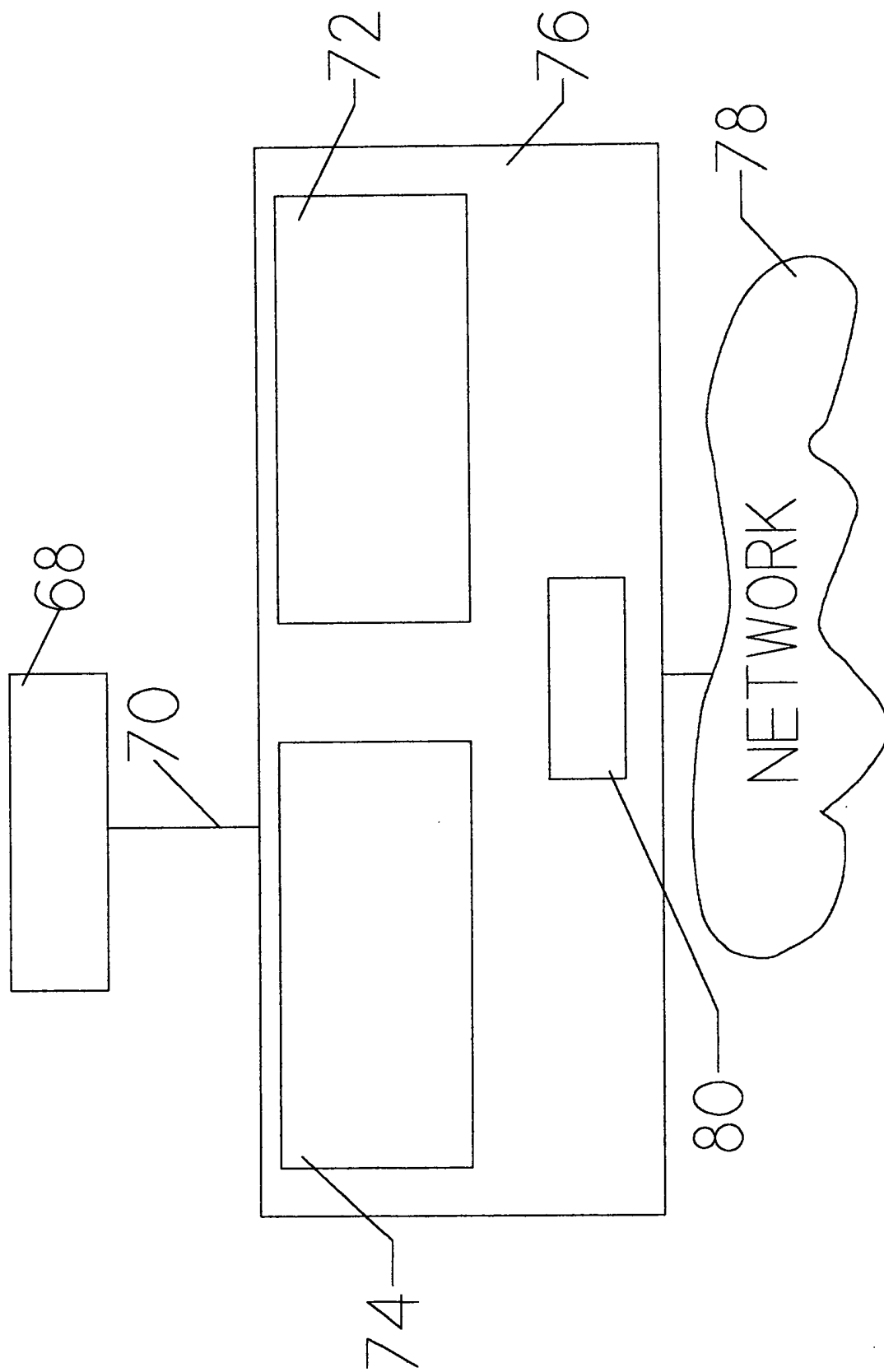


FIG. 5

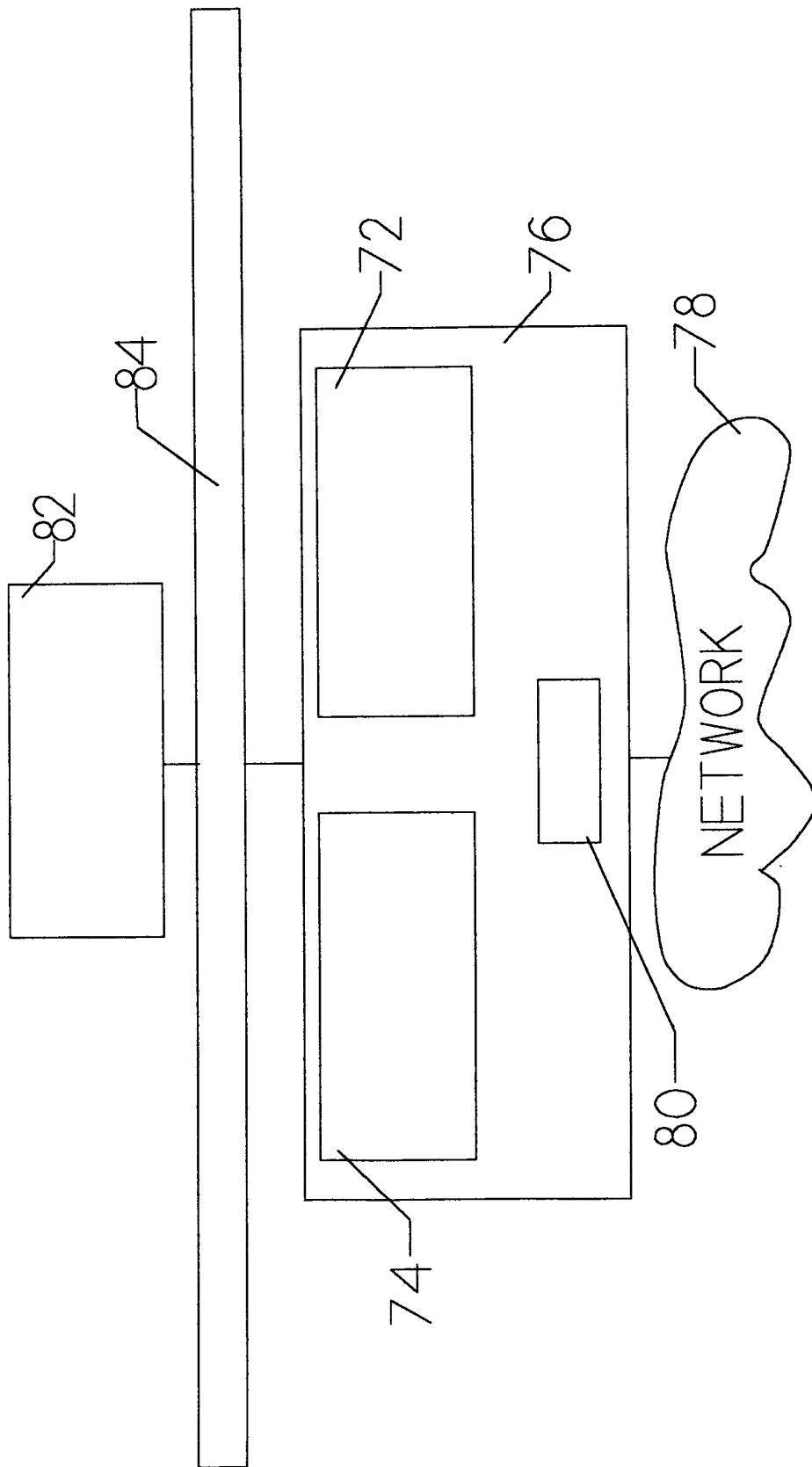


FIG. 6

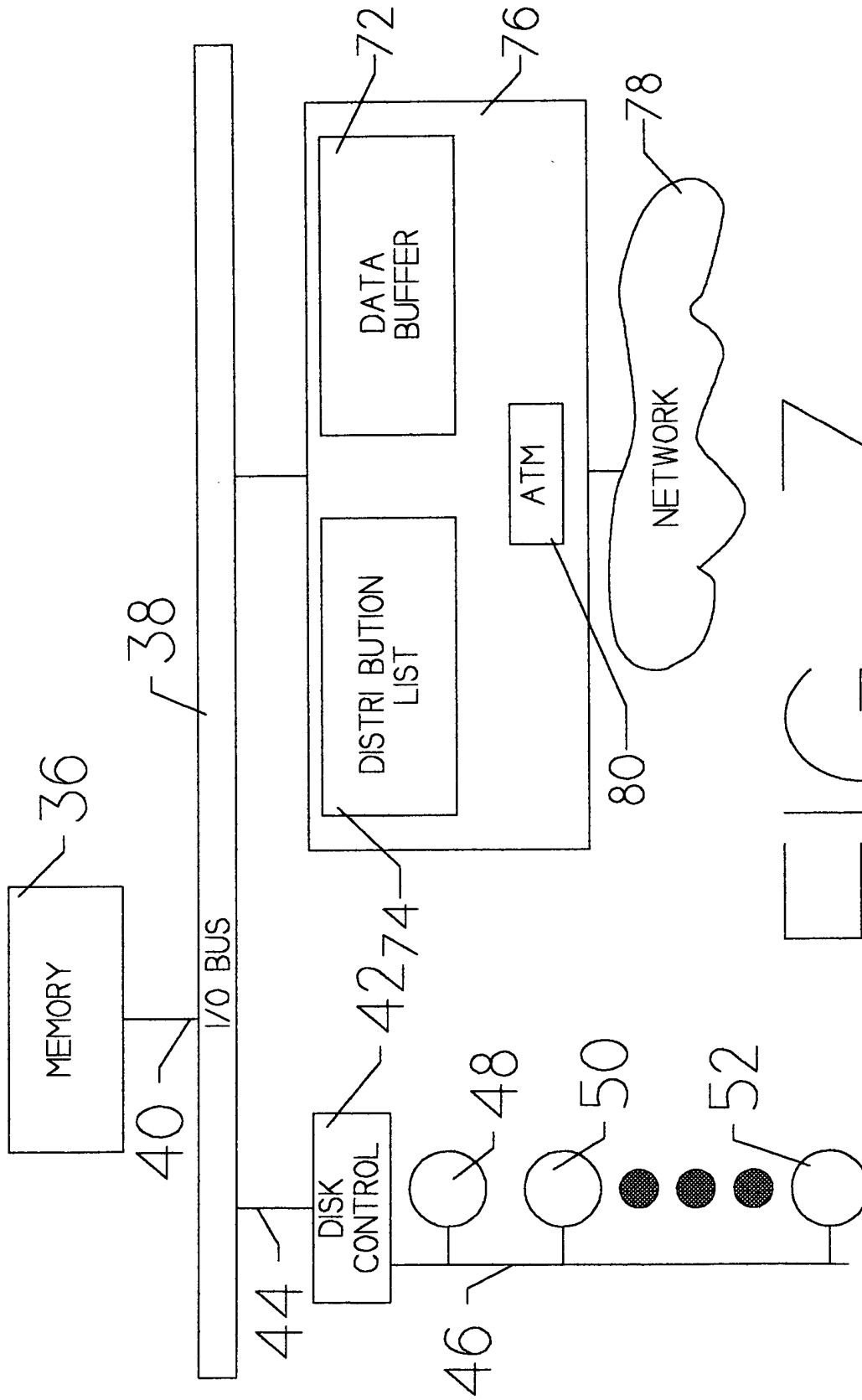


FIG. 7

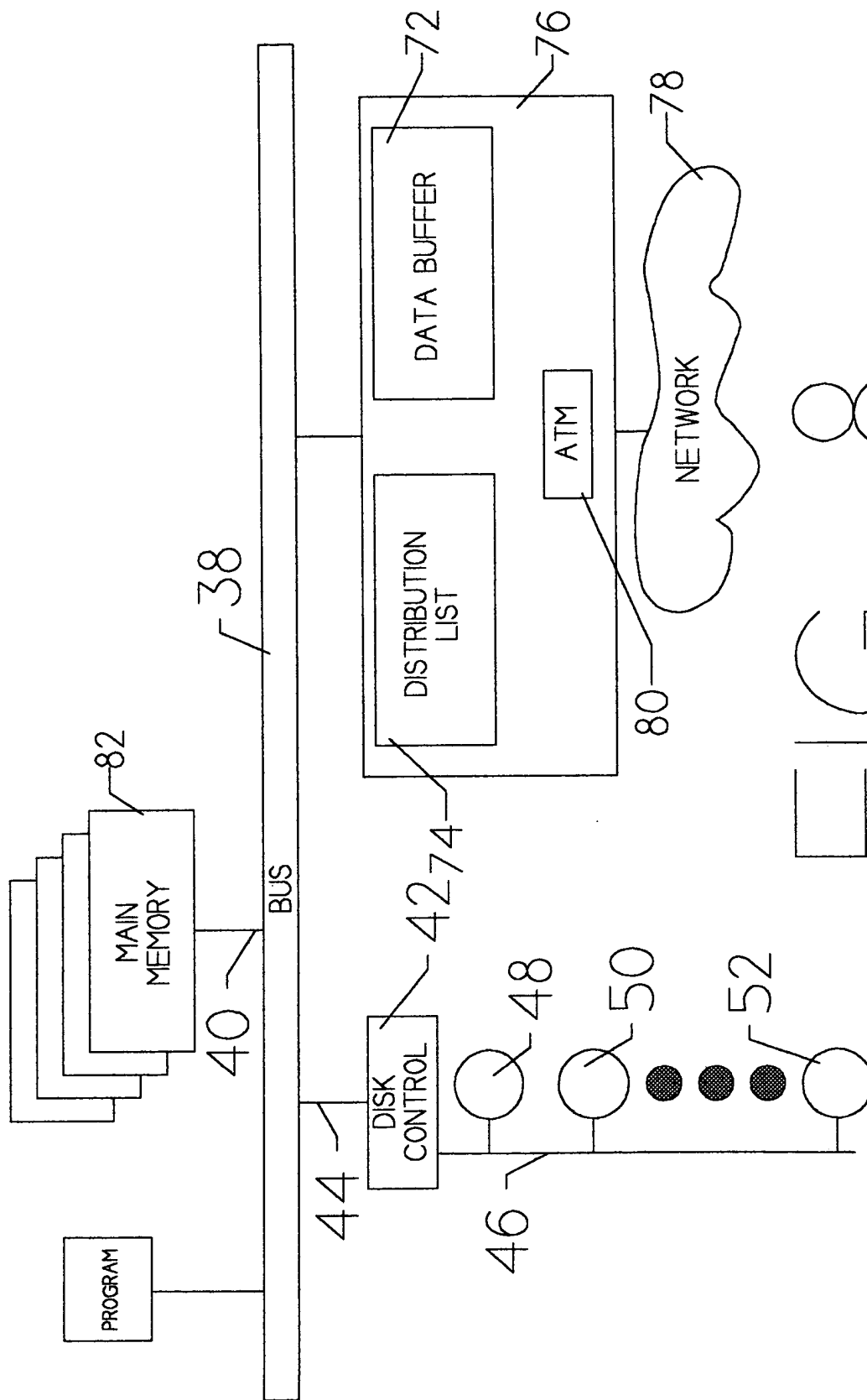


FIG. 8

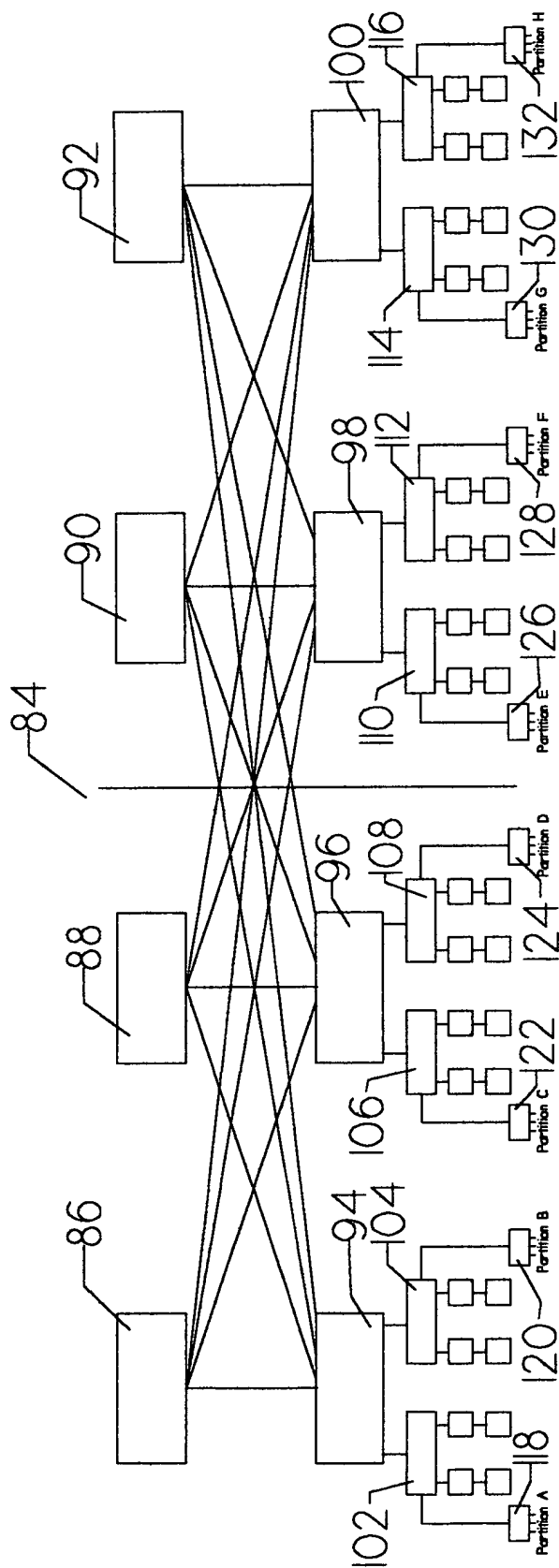


FIG. 9

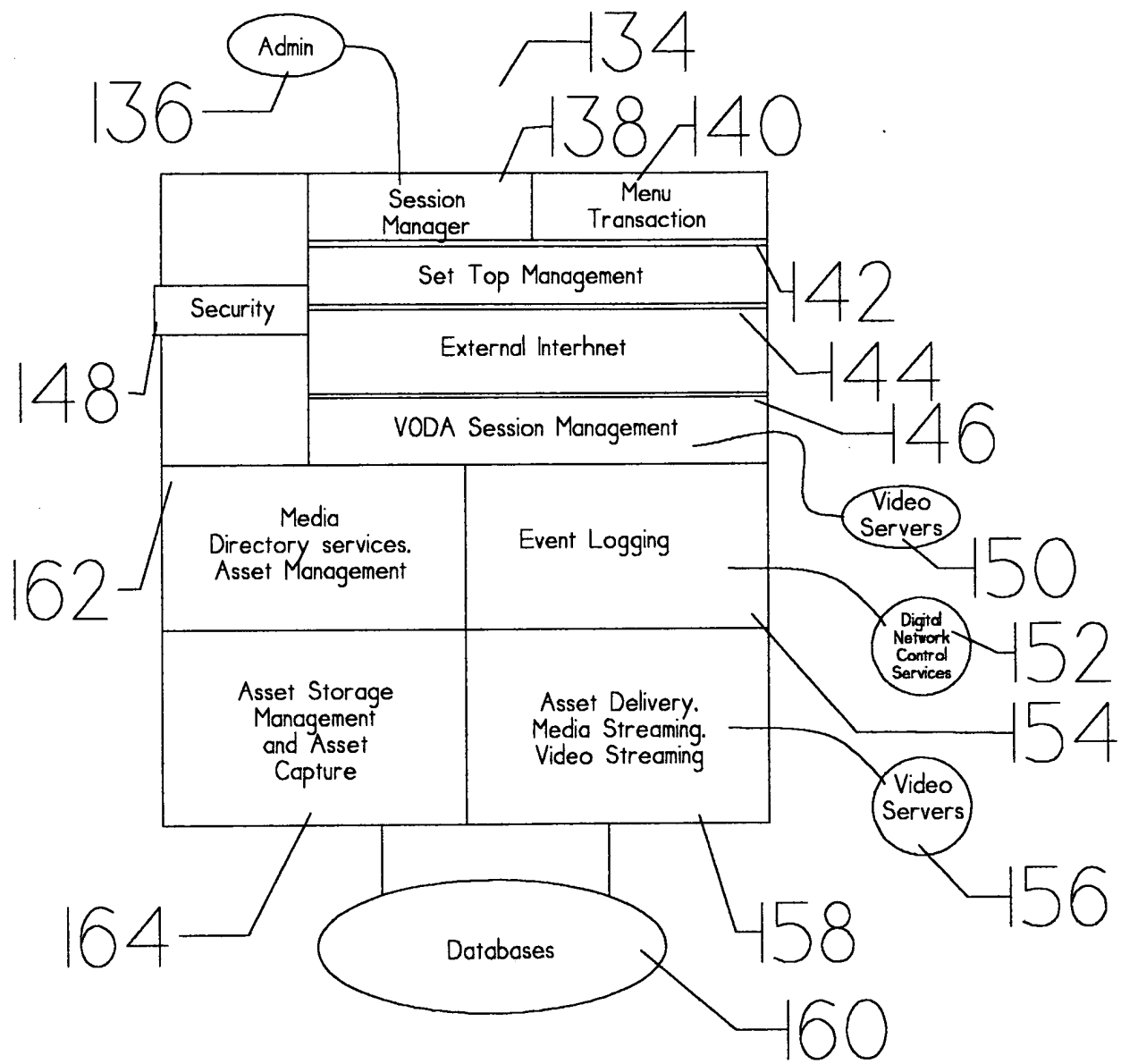


FIG. 10

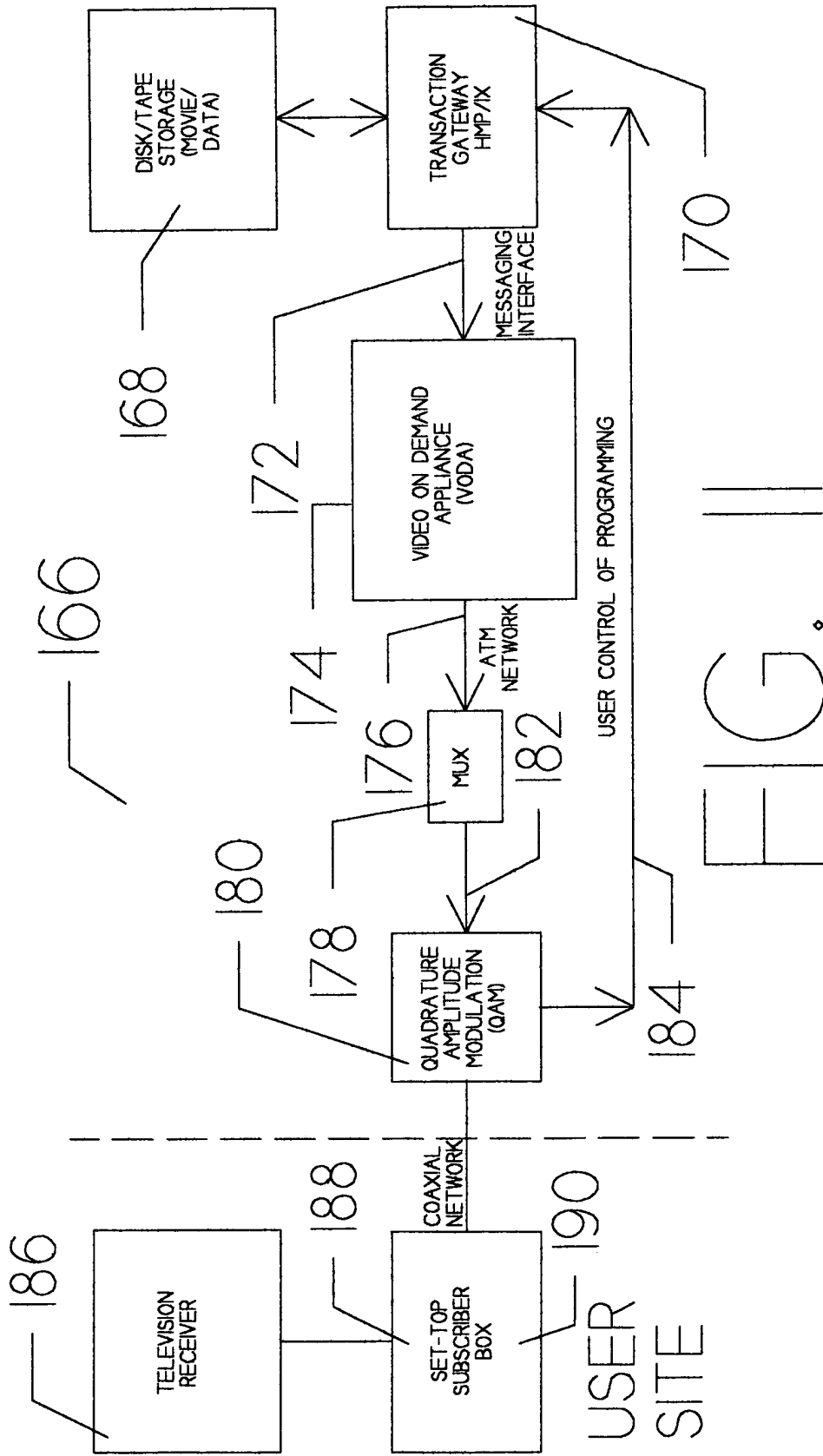


FIG. 1

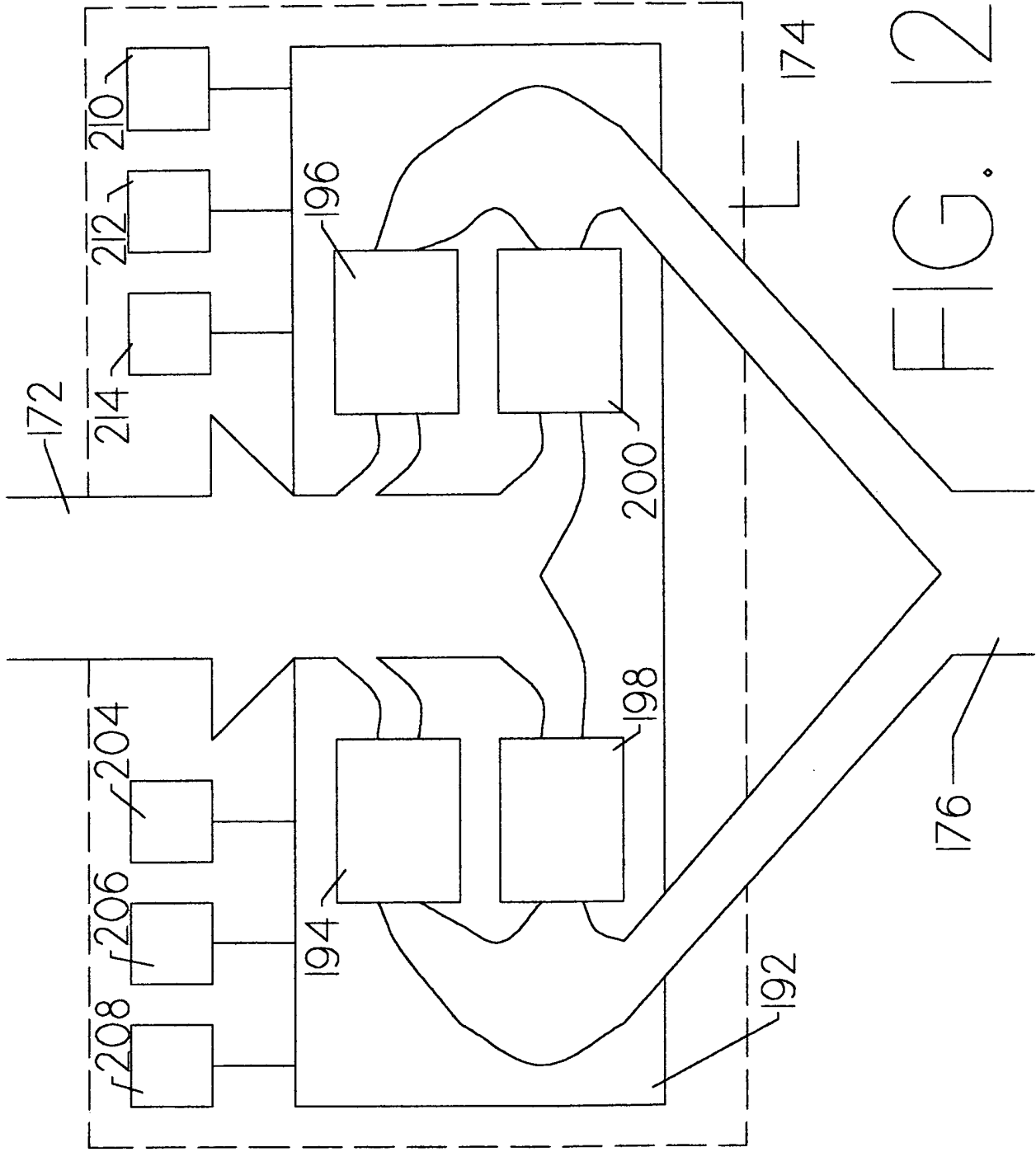


FIG. 12